

Rakun LS1 Qseven module

Rakun is available as Qseven module or IP building block.

We provide custom QUICC Engine microcodes to support new functions and protocols.



Industrial grade SBC Qseven module built on **Freescale LS1021A** based on dual ARM Cortex-A7 cores.

QUICC Engine - microcode driven RISC engine to support many standard and custom hard real-time protocols, ideal for demanding industrial protocols.

More info: www.borea.si/rakunls1



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Features:

- Qseven form factor
- Freescale LS1021A dual core ARM Cortex-A7 CPU + QE RISC
- up to 2GB DDR3L memory with ECC
- dual 32MB qSPI NOR Flash
- up to 2GB NAND Flash
- 3 10/100/1000 Ethernet interfaces
- up to 8 UART interfaces
- USB 3.0 or USB 2.0 OTG
- 2 PCIe Gen2 interfaces
- SATA 3.0 interface
- Display interface / HDMI
- HDLC, TDM, I2C, ESDHC/MMC/eMMC, SPI, CAN, PWM, I2S, S/PDIF, GPIOs

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